



PRESIDENCY UNIVERSITY, BENGALURU

SCHOOL OF ENGINEERING

PROGRAM STRUCTURE FOR M.TECH IN EMBEDDED SYSTEM AND VLSI

SEMESTER I

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
1.		Mathematics - 1	3	0	0	3
2.		Embedded System Design	3	0	2	4
3.		Software for Embedded Systems (4)	4	0	0	4
4.		CMOS VLSI Design (4)	3	0	2	4
5.		Advanced Digital System Design (4)	4	0	0	4
6.		Research Methodology	3	0	0	3
7.		Seminar – I	0	0	0	1
		Total	20	0	4	23

SEMESTER II

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
1.		Mathematics - 2	3	0	0	3
2.		Hardware Software Codesign	3	0	2	4
3.		Embedded Real Time Operating Systems	3	0	2	4
4.		Low Power VLSI Design	4	0	0	4
5.		Discipline Elective - 1	3	0	0	3
6.		Open Elective – 1	3	0	0	3
7.		Seminar – 2	0	0	0	1
		Total	19	0	4	22

SEMESTER III

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
1.		Design for Testability	4	0	0	4
2.		Discipline Elective - 2	3	0	0	3
3.		Discipline Elective - 3	3	0	0	3
4.		Open Elective – 2	3	0	0	3
5.		Phase – 1 (Minor) Project	0	0	0	5
		Total	13	0	0	18

SEMESTER IV

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
1.		Phase – 2 (Major) Project	0	0	0	15
		Total				15

Elective Courses (Discipline Electives and Open Electives)

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
1.		CAD for VLSI (DE - 1)	3	0	0	3
2.		ASIC Design and Modelling	3	0	0	3
3.		Reconfigurable Computing	3	0	0	3
4.		VLSI Architecture	3	0	0	3
5.		Networked Embedded Applications	3	0	0	3
6.		Processor Design	3	0	0	3
7.		Wearable Computing (DE - 2)	3	0	0	3
8.		Embedded Intelligence	3	0	0	3
9.		VLSI Signal Processing (DE - 3)	3	0	0	3
10.		Network Security	3	0	0	3
11.		IC Fabrication Technology	3	0	0	3
12.		Advanced Computer Networks	3	0	0	3
13.		MEMS and Nanotechnology	3	0	0	3
14.		Software Defined Radio	3	0	0	3
15.		Memory Design	3	0	0	3
16.		Pervasive Computing	3	0	0	3